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Substitute Form PTO-1449  
(Modified)

Sheet 1 of 2

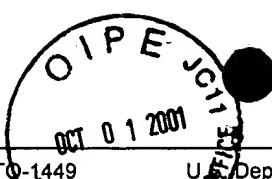
Substitute Form PTO-1449 (Modified)		U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 04899-043001	Application No. 09/847,391
<b>Information Disclosure Statement by Applicant</b> (Use several sheets if necessary) (37 CFR §1.98(b))		Applicant Vijaya Raghavan et al.		
		Filing Date May 2, 2001	Group Art Unit	

U.S. Patent Documents							
Examiner Initial	Desig. ID	Patent Number	Issue Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

Foreign Patent Documents or Published Foreign Patent Applications							
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation
							Yes
	AL						
	AM						
	AN						
	AO						
	AP						

Other Documents (include Author, Title, Date, and Place of Publication)		
Examiner Initial	Desig. ID	Document
TD	AQ	Drusinsky, "Specification IS Verification", presentation. /
TD	AR	Drusinsky-Yoresh, "Specification-based Verification of Reactive Systems", Disclosure. ✓
TD	AS	Harel et al., "Executable Object Modeling with Statecharts", IEEE Computer Vol. 30, No. 7, pp. 31-42, 1997. /
TD	AT	Harel et al., "On the Formal Semantics of Statecharts", IEEE, pp. 54-64, 1987. /
TD	AU	Harel, "On Visual Formalisms", Communications of the ACM, Vol. 31, No. 5, 514-175, 1988. /

Examiner Signature <i>Unreliable</i>	Date Considered 2/25/04
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	



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<b>Other Documents (include Author, Title, Date, and Place of Publication)</b>		
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TD	AV	Harel, "Statecharts: A Visual Formalism for Complex Systems", Elsevier Science Publishers B.V. (North Holland), pp 231-257, 1987.
TD	AW	Henzinger et al., "Temporal Proof Methodologies for Real-time Systems", ACM, pp. 353-366, 1990.
TD	AX	Kang and Ko, "PARTS: A Temporal Logic-Based Real-Time Software Specification, and Verification Method", Department of Computer Science and Engineering Pohang University of Science and Technology, ICSE 1995 Seattle, WA, pp. 169-175.
TD	AY	Time-Rover, "Temporal Logic: Time is of the Essence", <a href="http://www.time-rover.com/ltl.html">http://www.time-rover.com/ltl.html</a> , 9/6/99.
TD	AZ	Tuzhilin, "Extending Temporal Logic to Support High-Level Simulations", ACM Transactions on Modeling and Computer Simulation, Vol. 5, No. 2:129-155, 1995.
TD	AAA	Manz and Pnueli, "The Temporal Logic of Reactive and Concurrent Systems", Springer-Verlag, 1991.

Examiner Signature <i>Alvarado</i>	Date Considered 2/25/04
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